



XBERT Series Bit Error Rate Tester Datasheet

SL3010A NRZ BERT

SL3040A NRZ/PAM4 BERT

SL3000A NRZ/PAM4 BERT



XBERT Series Bit Error Rate Tester provides high performance, extensive functions, flexible option and ultra-high integration. It can provide high performance and flexibility for the R&D, debugging and production testing of high-speed serial circuits.

XBERT supports data rate continuously adjustable with 1Kbps step, It's guaranteed to ensure flexibility and comprehensiveness of testing. This feature makes it possible to test the margin and limit parameters of device under test(DUT) effectively . And PPG supports several taps de-emphasis for PAM4 and NRZ signals to compensate for the loss of the signal during transmission and improve signal quality. The BERT integrates an internal clock recovery module which helps to maintain the link stability when running BER measurement. This has made XBERT reach a rigorous requirement for bit error rate which is less than 1e-15 and got the accuracy test result in harsh and complex test environments

User Interface



User Interface can bring the availability up to a new level

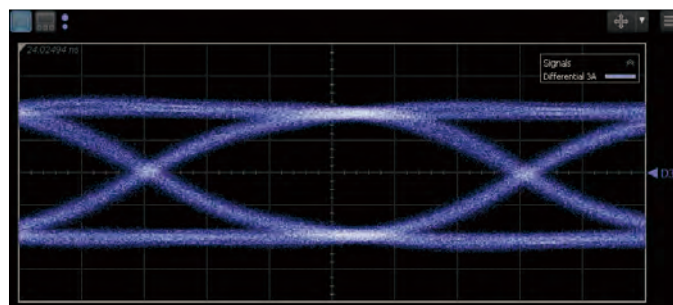
- Simple and convenient guiding function
- Humanized layout and operation process
- Convenient and flexible interface switching
- Abundant test items
- Connected to external computers via USB



SL3010A NRZ BERT

- PPG supports 1Gbps to 28Gbps NRZ data rate, continuously adjustable with 1kbps step
- PPG supports 4 or 8 channels (SL3020A supports up to 8 channels)
- Ultra low output jitter (instinct random jitter $R_j < 1.1$ ps @28Gbps)
- PPG differential output amplitude: 100mV ~ 1V
- Test pattern supports prbs7/9/10/13/15/23/31 and 64 bit user defined pattern
- BERT input sensitivity: 40mV
- SL3010A supports BER gating time setting and advanced analysis features (Eye height, Eye width, Eye Open)

Test Results



NRZ PPG 25.78125Gbps test result (PRBS31)

Product Specifications

PPG

NRZ Output Data Rate	1 Gbps-28Gbps
Data Rate Resolution	Continuously adjustable with 1 kbps step
Rj (RMS)	≤ 1.1 ps@28Gbps
Rise/Fall Time	16ps(typ)@28Gbps
Output Amplitude(Differential)	100mV-1000mV
Polarity Reversal	Supportive
Emphasis	3 Tap
Pattern Output	PRBS7, PRBS9, PRBS15, PRBS23, PRBS31, 64bit user defined pattern

ED

Data Rate	1 Gbps-28Gbps NRZ
Maximum Differential Voltage Input	1.2V
Input Sensitivity	40mV
Pattern Input	PRBS7, PRBS9, PRBS15, PRBS23, PRBS31

Others

Output Port	2.92mm female
Clock Input	50MHz-400MHz, 600mV±200mV @ 50Ω
Clock Output Ratio	2/4/8/16

Size (Gaskets not included)

Width	228.5mm
Depth	176mm
Height	88mm
Weight	≤3kg

Ordering and Configuration

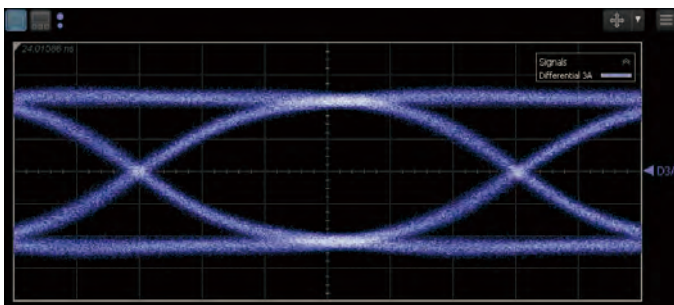
Product Code	Description	Configuration
SL3010A	4-Channel BERT Mainframe	Mainframe
Option NP04A	4-Channel NRZ Pattern Generator	Option
Option NB04A	4-Channel NRZ BERT	Option
SL3020A	8-Channel BERT Mainframe	Mainframe
Option NP08A	8-Channel NRZ Pattern Generator	Option
Option NB08A	8-Channel NRZ BERT	Option



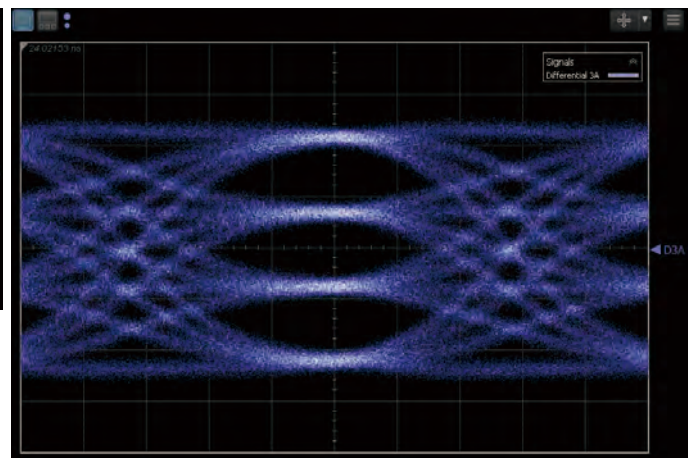
SL3040A NRZ/PAM4 BERT

- Dynamically switch between PAM4 and NRZ mode
- PPG supports 22Gbaud-29.6Gbaud PAM4 data rate
- PG supports 9Gbps to 14Gbps and 18-29.6Gbps NRZ data rate
- PPG supports 4 or 8 channels
- Ultra low output jitter (instinct random jitter $R_j < 0.8\text{ps}$ @28Gbps)
- The maximum output amplitude of PPG pattern generator is selectable from 0.5V/1V
- Test pattern support (prbs7/9/11/13/15/23/31, prbs7Q/9Q/11Q/13Q/15Q/23Q/31Q, SSPRQ and User Defined Pattern)
- BERT input sensitivity: 50mV

Test Results



NRZ PPG 25.78125Gbps test result (PRBS31)



PAM4 PPG 26.5625Gbaud test result (PRBS31)

Product Specifications

PPG

NRZ Output Data Rate	9Gbps-14Gbps, 18Gbps-29.6Gbps
PAM4 Output Data Rate	22Gbaud-29.6Gbaud
Data Rate Resolution	Continuously adjustable with 1kbps step
Rj (RMS)	≤800fs@28Gbps
Rise/Fall Time	15ps(typ)@28Gbps
Output Amplitude(Differential)	500mV/1000mV
Polarity Reversal	Supportive
Emphasis	Supports up to 7 Tap
Pattern Output	PRBS7/9/11/13/15/23/31, PRBS7Q/9Q/11Q/13Q/15Q/23Q/31Q, SSPRQ,UDP

ED

Data Rate	NRZ:9Gbps-14Gbps, 18Gbps-29.6Gbps; PAM4:22Gbaud-29.6Gbaud
Maximum Differential Voltage Input	1.2V
Input Sensitivity	50mV
Pattern Input	PRBS7, PRBS9, PRBS10, PRBS13, PRBS15, PRBS23, PRBS31

Others

Output Port	2.92mm female
Clock Input	50MHz-400MHz, 600mV±200mV @ 50Ω
Clock Output Ratio	4/8/16/32/64/128

Size (Gaskets not included)

Width	444.5mm
Depth	348mm
Height	88mm
Weight	≤5kg

Ordering and Configuration

Product Code	Description	Configuration
SL3040A	NRZ/PAM4 BERT Mainframe	Mainframe
Option SL3040A-4A	4-Channel 500mV NRZ/PAM4 BERT	Option
Option SL3040A-4B	4-Channel 1000mV NRZ/PAM4 BERT	Option
Option SL3040A-8A	8-Channel 500mV NRZ/PAM4 BERT	Option
Option SL3040A-8B	8-Channel 1000mV NRZ/PAM4 BERT	Option



SL3000A NRZ/PAM4 BERT

The SL3000A is modularized high end BERT (Bit Error Rate Tester) for the high speed serial data physical layer characterization, testing and issue debugging. It provides a fully integrated all-in-one solution with maximum flexibility and performance.

The SL3000A supports the latest NRZ and PAM4 signaling rates up to 56GBaud. The target application is to test the mature and newly emerging standards such as OIF-CEI from 1Gbps to 58GBaud, IEEE Ethernet from 1.25Gbps to 53.125GBaud, and PCIE Gen 1/2/3/4 from 2.5Gbps to 16Gbps, JESD and CPRI.

- Compact form factor to save the bench space and DUT accessibility without remote head
- NRZ and PAM4 switchable from BERT software
- Data rate coverage from 1Gbps to 56GBaud
- Modularized design to maximum the flexibility and test coverage
- Multi-channel capability to maximum the test efficiency
- Advanced jitter injection capability
- Advanced noise injection capability
- Channel-skew adjustment
- High voltage output
- DC offset control
- Internal and external reference clock support with Precision Reference Option
- Gray coding and polarity control
- PRBS, PRBS-Q and customized test pattern support
- PPG EQ and ED input EQ support

BERT Mainframe



SL3000A is the host model of XBERT series with pluggable module design, which greatly enhances the flexibility of function expansion while without compromising the performance. The SL3000A also supports a variety of advanced features: inter-channel delay adjustment, high voltage output, direct bias adjustment, jitter injection, etc. It is a high-end laboratory-level measurement instrument in the XBERT series of high-speed serial error coders.

There are three mainframe models SL3000A, SL3002A and SL3004A available depending on the application, and each mainframe has a rack-mounted option for quick conversion from bench top to rack-mounted instruments.

SL3000A

The SL3000A mainframe hosts a 2U height and a half-rack design.

Size (Gaskets not included)	
Depth	492mm
Width	220mm
Height	88mm
Others	
Slot Number	2
Built-in system	None

SL3002A

The SL3002A mainframe hosts a 2U height with a full-rack design, and an option to upgrade the built-in WINDOWS operating system.

Size (Gaskets not included)	
Depth	492mm
Width	440mm
Height	88mm
Others	
Slot Number	8
Built-in system	None or Windows

SL3004A

The SL3004A mainframe hosts a 4U height with a full-rack design, and an option to upgrade the Windows built-in operating system.

Size (Gaskets not included)	
Depth	492mm
Width	440mm
Height	176mm
Others	
Slot Number	16
Built-in system	None or Windows

General Characteristics

Environmental Requirement	
Operation Temperature	0°C ~ +55°C
Storage Temperature	-20°C to +70°C
Operation Humidity	No condensation, 35°C, 20% ~ 80%

Power Supply	
Supply Voltage	100Vac ~ 240Vac
Voltage Frequency	45Hz ±5Hz

Module Selection



SL30164A

A 4-channel optical port BERT module, the speed range is continuously adjustable from 1 to 16Gbps, and the step size is 1kbps.

Performance index

Channel	4	Built-in clock recovery module	Support
Output Port	SFP	Receiver ED equalization	Support
Data Rate Range	1Gbps-16Gbps	ESD protection	Support
Pattern Type	PRBS7/9/15/23/31/UDP		

SL30174A

A 4-channel electric port error code test plug-in card module, the speed range is continuously adjustable from 1 to 16Gbps, and the speed step is 1kbps.

Performance index

Channel	4	Built-in clock recovery module	Support
Output Port	SMA port	Receiver ED equalization	Support
Signaling Type	NRZ	ESD protection	Support
Data Rate Range	1Gbps to 16Gbps		
Trigger Output	2/4/8/16 bit Variable		
Pattern Type	PRBS7/9/15/23/31/UDP		
Diff Output Amplitude	0.1Vpp-1Vpp		
Amplitude Step Size	40mVpp		
Coupling mode	AC coupling		
Rise/Fall Time	23ps@16Gbps		
Input Sensitivity	50mV		
Maximum Input Amplitude	1.2V		

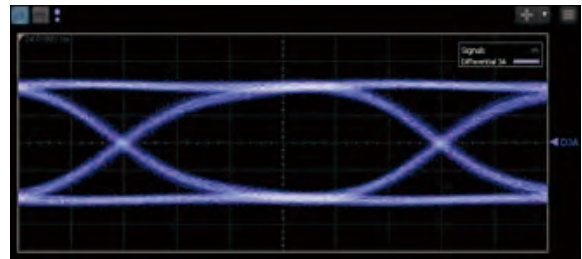


SL30074B

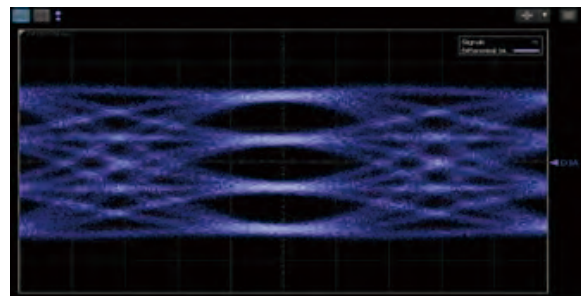
A 4-channel electric port error code test plug-in card module, the speed range is continuously adjustable from 1 to 32Gbps, and the speed step is 1 kbps.

Performance index

Channel	4	Built-in clock recovery module	Support
Output Port	2.92 mm port	Receiver ED equalization	Support
Signaling Type	NRZ	ESD protection	Support
Data Rate Range	1 Gbp-32Gbps		
Trigger Output	2/4/8/16 variable dividing ratio		
Pattern Type	PRBS7/9/10/13/15/23/31/UDP		
Diff Output Amplitude	0.1Vpp-1Vpp		
Amplitude Step Size	20mVpp		
Coupling mode	AC coupling		
Rise/Fall Time	14ps@28Gbps		
Input Sensitivity	25mV		
Maximum Input Amplitude	1.2V		



NRZ PPG 25.78125Gbps Test result (PRBS31)



PAM4 PPG 26.5625Gbaud Test Result (PRBS31)

SL3000A NRZ/PAM4 BERT

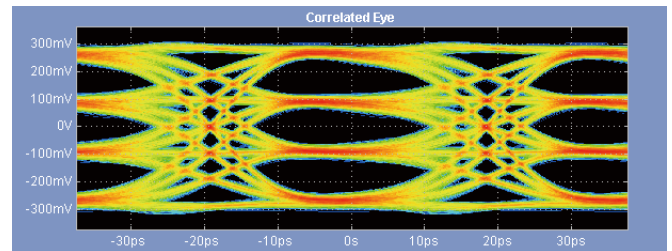
SL30374C

4-channel BERT module, speed range 23.5GBd to 28.5GBd, 50.2GBd to 57GBd NRZ/PAM4 continuously adjustable, speed step size is 1kbps.

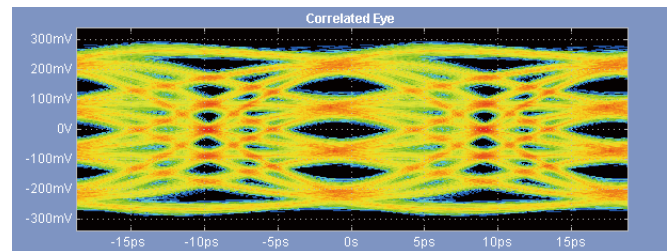
Performance index

Channel	4
Output Connector type	2.4mm female
Signaling Type	NRZ/PAM4
Data Rate Range	23.5GBd-28.5GBd, 50.2GBd-57GBd
Trigger Output	4/8/16/32/128 variable dividing ratio
Pattern Type	PRBS7/9/11/13/15/16/23/31, JP03A/JP03B/LINEARITY/CID/SSPRQ, 64bit UDP
Diff Output Amplitude	0.1Vpp-0.8Vpp
Amplitude Step size	< 10mVpp
Coupling mode	AC coupling
Rise/Fall Time	10ps@56GBaud
Input Sensitivity	25mV
Maximum Input Amplitude	1.2V

Built-in clock recovery module	Support
Receiver ED equalization	Support
ESD protection	Support



PAM4 PPG 26.5625Gbaud Test Result (PRBS13Q)



PAM4 PPG 53.125Gbaud Test Results (PRBS13Q)

SL33071B

Single-channel BERT module, supports 1-33Gbps continuous adjustable, rate step size is 1kbps with built-in pickoff-tees.

Performance index

Channel	1
Output Connector type	2.92mm electrical port
Signaling Type	NRZ
Data Rate Range	1 Gbps- 33Gbps
Input Sensitivity	50mV single-ended eye height (typ)
Data Insertion Loss	4.0dB (typ) 6.0dB (max)
Maximum Input Amplitude	± 1.2V
Equalization range	0-20dB
Loop Bandwidth	≥25MHz
Coupling mode	DC Coupling

Clock Output Ratio	2/4/8/16/32
Output Amplitude	0.1Vpp-1Vpp (Differential)
Amplitude step	20mVpp(typ)
Coupling mode	AC Coupling
Receiver ED equalization	Support
ESD protection	Support

SL33171B

Single-channel BERT module, supports 1-16Gbps continuous adjustable, rate step size is 1kbps with built-in pickoff-tees.

Performance index

Channel	1	Clock Output Ratio	2/4/8/16/32
Output Connector Type	SMA port	Diff Output Amplitude	0.1Vpp-1Vpp Differential
Signaling Type	NRZ	Amplitude step	20mVpp (typ)
Data Rate Range	1 Gbps-16Gbps	Coupling mode	AC Coupling
Input Sensitivity	50mV Differential (typ)	Receiver ED equalization	Support
Insertion Loss	4.0dB (typ) 6.0dB (max)	ESD protection	Support
Maximum Input Amplitude	± 1.2V		
Equalization range	0 to 20dB		
Loop Bandwidth	≥25MHz		
Coupling mode	AC Coupling		

Options

Product Code	illustrate	Remark
OPTION- AP0040A	PPG High Voltage option	Scalable output amplitude to differential 2Vpp
OPTION- BT0040A	DC bias option	Scalable Output to DC Coupled, Bias Voltage -2V to +2V
OPTION- DL0040A	Phase/Skew adjustment option	Delay adjustment accuracy: 100fs (typ) Delay adjustment range depends on output rate
OPTION-REF100D	Precision Reference Option	Support internal and external reference switching Reference clock frequency: 10MHz, clock stability: $\leq \pm 5e-8$
OPTION-SJ01A	Sinusoidal Jitter Injection Option	Support sinusoidal jitter frequency range 1kHz to 5MHz, jitter amplitude greater than 100UI@100kHz
OPTION TB0001A	USB3.0 test tooling	
OPTION TB0002A	SFP28 Test tooling	
OPTION TB0003A	QSFP Test tooling	
OPTION C292	2.92mm Cable Assembly	
OPTION C240	2.4mm Cable Assembly	
OPTION T50	50ohm load	
OPTION LTW-08090	torque wrench	
OPTION HTC	Reinforced packing box	
OPTION W3	3 years warranty	
OPTION W5	5 years warranty	

General Technical Data

Unless otherwise stated, all of the following technical data applies to all models:

Environmental Requirements

Operation Temperature	0°C ~ +55°C
Storage Temperature	-20°C ~ +80°C
Operation Humidity	noncondensing, 35°C, 20~80%
Power Supply	100-240Vac, 45-55Hz

Configuration

Product Code	Description	Configuration
Option-P292	Panel Connector: 2.92mm-K	Option
Option-P240	Panel Connector: 2.4mm-K	Option
Option-AMP0040A	High Amplitude Module	Option
Option-BT0040A	DC Offset Module	Option
Option-DL0040A	Phase Control Module	Option
Option-REF100D	10MHz Reference Module	Option
Option TB0001A	USB3.0 Test Board	Option
Option TB0002A	SFP28 Test Board	Option
Option TB0003A	QSFP Test Board	Option
Option C292	2.92mm-JJ Cable Components	Option
Option C240	2.92mm-JJ Cable Components	Option
Option T50	50ohm Load	Option
Option LTW-08090	Torque Wrench	Option
Option HTC	Crates Reinforcement	Option
Option W3	3-year warranty	Option
Option W5	5-year warranty	Option

For more information on Sinolink Technologies' products, applications or services please contact Sinolink Technologies (Beijing) Co., Ltd.
The complete list is available at: www.sinolink-technologies.com



Sinolink Technologies (Beijing) Co., Ltd.

Address: Rm1403, Tower C, No.15 Ronghua South Road, BDA, Beijing, 100176, P.R. China

Tel: 86-10-81028321

Fax: 86-10-81028322

WhatsApp: 86-18800101219

Email: sales@sinolink-technologies.com

Postal Code: 100176

www.sinolink-technologies.com



*Sinolink Technologies reserves the rights to change product specifications and pricing.
All related trademarks are service marks or registered trademarks of respective companies.*

Website



*1 Year Warranty
The combination of superior product reliability and 1-year warranty service helps you achieve the following goals:
increased measurement confidence, reduced cost of ownership, and increased ease of operation.*